



Test Report

Sample Name: The integrated circuit

Part Number: XC6VSX315T-1FFG1156I

Manufacturer: XILINX

Customer: _____

Shenzhen Chuangxin Online Testing Service Co., Ltd.

December 04, 2025



Test Report

Customer:

Customer Address:

Sample Name: The integrated circuit

Part Number: XC6VSX315T-1FFG1156I

Manufacturer: XILINX

Date Code: N/A

Package Type: BGA-1156

Quantity Received: 25 PCS

Quantity Inspected: 25 PCS

Date Received: 11/27/2025

Date Tested: 11/27/2025/15: 00 - 11/27/2025/16: 10

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Tested by _____

Issued by

Inspected by _____

(Stamp)

Approved by _____

Test Items And Conclusions

Results Analysis			Conclusion
According to the requirements of the client, 25 samples were subjected to one type of inspection: Programming test. None of the one inspection revealed any standard defects.			✓
Test Items	Quantity (PCS)	Test Standard	Result
<input checked="" type="checkbox"/> Documentation and Packaging inspection	25	AS6081A-2023	✓
<input type="checkbox"/> External visual inspection	N/A	N/A	N/A
<input type="checkbox"/> Top permanency test	N/A	N/A	N/A
<input type="checkbox"/> Radiography(X-ray)	N/A	N/A	N/A
<input type="checkbox"/> XRF test	N/A	N/A	N/A
<input type="checkbox"/> Pin correlation test	N/A	N/A	N/A
<input checked="" type="checkbox"/> Programming test	25	AS6081A-2023	✓
<input type="checkbox"/> Key functional test(KFT)	N/A	N/A	N/A
<input type="checkbox"/> SAT test	N/A	N/A	N/A
<input type="checkbox"/> Solderability Analysis	N/A	N/A	N/A
<input type="checkbox"/> Reflow soldering	N/A	N/A	N/A
<input type="checkbox"/> Internal visual inspection	N/A	N/A	N/A

Result Status: ✓ Acceptable ✗ Nonconforming ● Suspicious N/A Not tested & not applicable

Test Equipment

Device name	Serial No.	Model No.	Calibration Due Date
Optical microscope	TC-S-WG-007	SEZ-260	2026/07/07
Digital-analog mixed signal testing machine	TC-S-GN-051	TR6850	2026/07/07
Datasheet Reference 《XILINX XC6VSX315T-1FFG1156I》 : https://docs.amd.com/v/u/en-US/ds152			

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1. Device description:

Virtex®-6 FPGAs are available in -3, -2, -1, and -1L speed grades, with -3 having the highest performance.

2. Package dimensions:

The following is for the **XC6VVSX315T**, XC6VLX365T, and XC6VVSX475T devices.

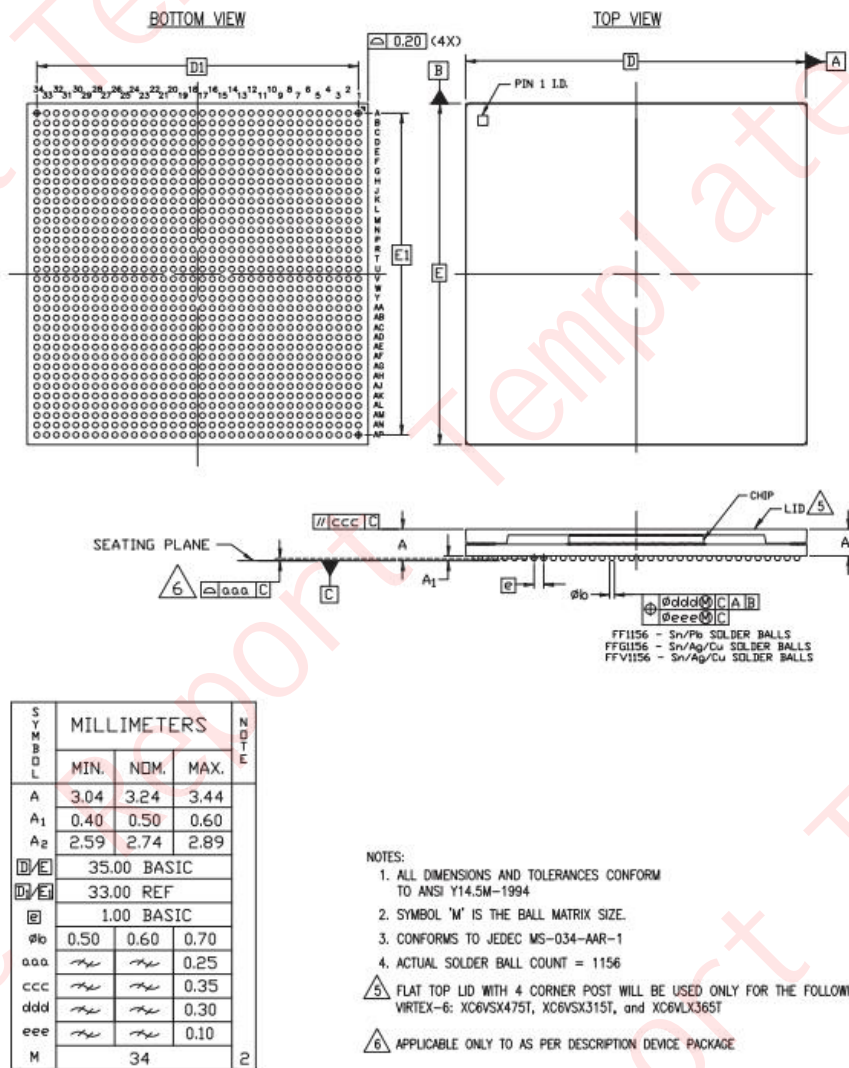


Figure 4-10: FF1156/FFG1156 Flip-Chip Fine-Pitch BGA Package Specifications

3. Documentation and Packaging inspection:

Outer Box	N/A	Outer Box Size (cm)	N/A
Inner Box	N/A	Inner Box Size (cm)	N/A
Total Weight	856 g	Date Code	N/A
COO	N/A	Material Number	N/A
Quantity Received	25 PCS	Quantity Inspected	25 PCS
Moisture Protection	N/A	ESD Protection	N/A
Sample No (Date Code + Serial No)	#1~#25		

Received view-1



Received view-2



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4. Programming test:

Applicable standard: **AS6081A-2023**

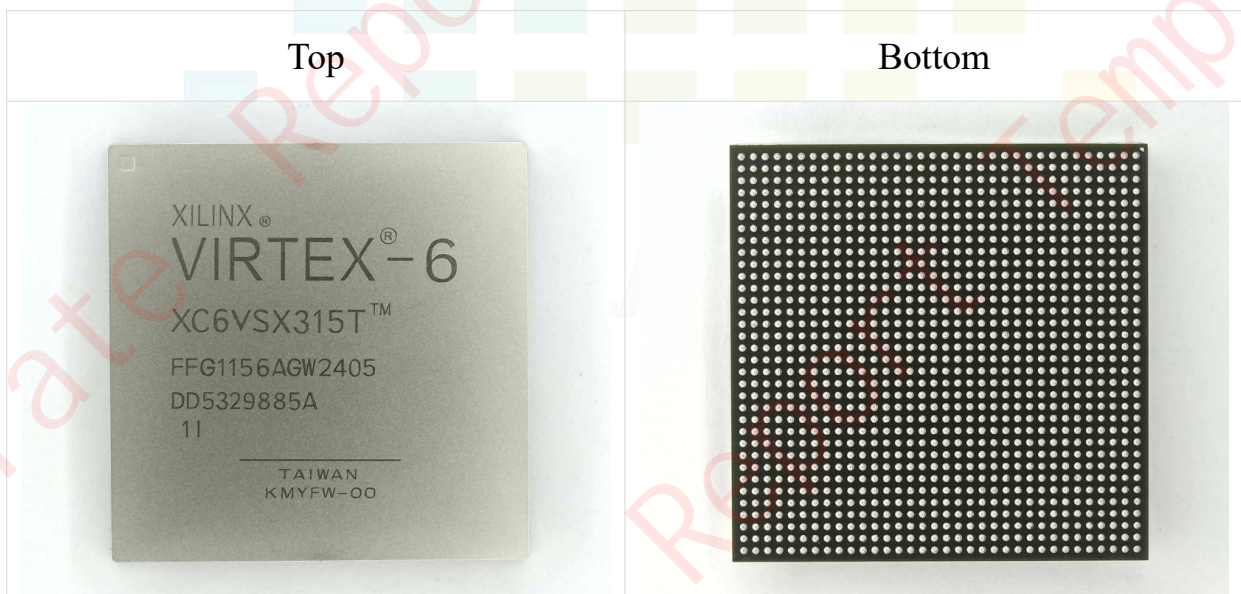
Ambient temperature: 25 ± 3 °C Relative humidity: 45% ~ 65% RH

The customer provided 25 PCS samples of **XILINX** Part Number **XC6VSX315T-1FFG1156I** for analysis and 25 PCS for programming test. The details are as follows:

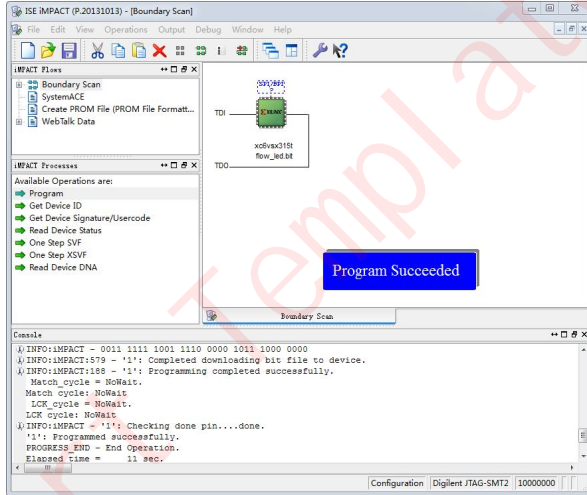
Using ISE Design Suite 14.7, JTAG connector, Platform Cable USB verified the following parameters:

- JTAG configuration mode
- Program
- Get Device ID
- Get Device Signature/Usercode
- Read Device Status

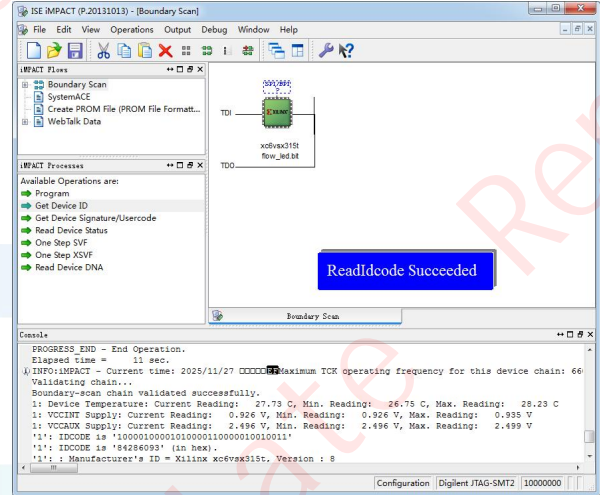
25 PCS samples passed programming test.



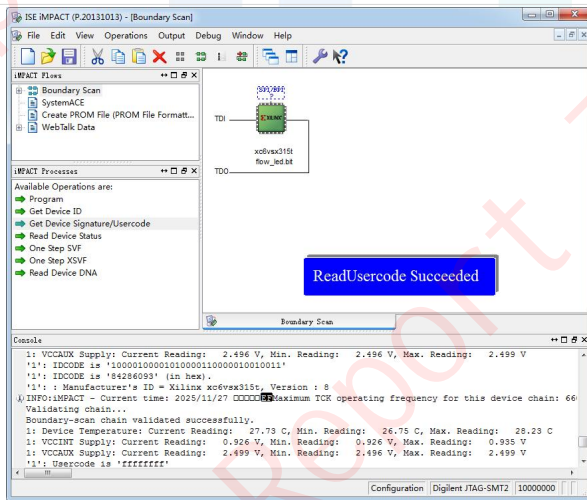
JTAG configuration mode & Programming test (Pass)



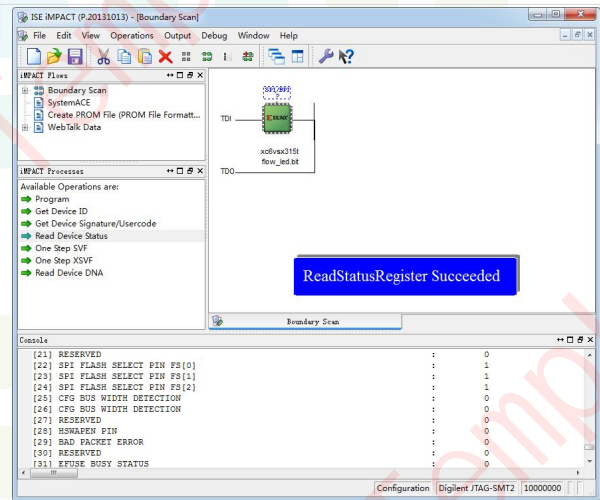
Get Device ID (Pass)



Get Device Signature/Usercode (Pass)



Read Device Status (Pass)



Programming test	Result:
Total quantity tested	25 PCS
Total quantity passed	25 PCS
Total quantity failed	0 PCS
Conclusion	25 PCS samples passed programming test.

Programming test conclusion:	
Tested parameters	Conclusion
JTAG configuration mode	Acceptable
Program	Acceptable
Get Device ID	Acceptable
Get Device Signature/Usercode	Acceptable
Read Device Status	Acceptable

-End of Report-

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